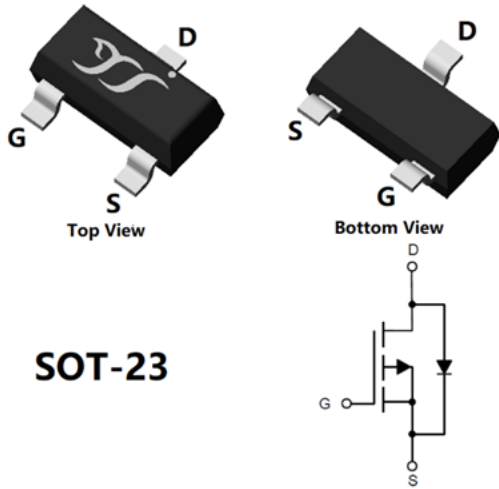


P-Channel Enhancement Mode Field Effect Transistor



SOT-23

Product Summary

- V_{DS} -100V
- I_D -0.8A
- $R_{DS(ON)}$ (at $V_{GS}=-10V$) <600m Ω
- $R_{DS(ON)}$ (at $V_{GS}=-4.5V$) <650m Ω

General Description

- Voltage controlled small signal switch
- Fast Switching Speed
- Moisture Sensitivity Level 1
- Epoxy Meets UL 94 V-0 Flammability Rating
- Halogen Free

Applications

- Power Management Functions
- Motor Control
- DC-DC convertor

■ Absolute Maximum Ratings ($T_J=25^\circ\text{C}$ unless otherwise noted)

| Parameter | | | Symbol | Limit | Unit |
|--|---|-------------------------|----------------|----------|------------------|
| Drain-source Voltage | | | V_{DS} | -100 | V |
| Gate-source Voltage | | | V_{GS} | ± 20 | V |
| Continuous Drain Current (Note 1,2) | Steady-State | $T_A=25^\circ\text{C}$ | I_D | -0.8 | A |
| | | $T_A=100^\circ\text{C}$ | | -0.5 | |
| Pulsed Drain Current | $T_C=25^\circ\text{C}$, $t_p=100\mu\text{s}$ | | I_{DM} | -6.4 | A |
| Total Power Dissipation (Note 1,2) | Steady-State | $T_A=25^\circ\text{C}$ | P_D | 1 | W |
| | | $T_A=100^\circ\text{C}$ | | 0.4 | |
| Junction and Storage Temperature Range | | | T_J, T_{STG} | -55~+150 | $^\circ\text{C}$ |

■ Thermal resistance

| Parameter | | Symbol | Typ | Max | Units |
|---|--------------|-----------------|-----|-----|--------------------|
| Thermal Resistance Junction-to-Ambient (Note 2) | Steady-State | $R_{\theta JA}$ | 105 | 125 | $^\circ\text{C/W}$ |

■ Ordering Information (Example)

| PREFERRED P/N | PACKING CODE | Marking | MINIMUM PACKAGE(pcs) | INNER BOX QUANTITY(pcs) | OUTER CARTON QUANTITY(pcs) | DELIVERY MODE |
|---------------|--------------|---------|----------------------|-------------------------|----------------------------|---------------|
| YJL600P10AJ | F2 | 600P10 | 3000 | 30000 | 120000 | 7" reel |



YJL600P10AJ

■ Electrical Characteristics (T_J=25°C unless otherwise noted)

| Parameter | Symbol | Conditions | Min | Typ | Max | Units |
|---------------------------------------|---------------------|---|------|------|------|-------|
| Static Parameter | | | | | | |
| Drain-Source Breakdown Voltage | BV _{DSS} | V _{GS} = 0V, I _D =-250μA | -100 | - | - | V |
| Zero Gate Voltage Drain Current | I _{DSS} | V _{DS} =-100V, V _{GS} =0V | - | - | -1 | μA |
| | | V _{DS} =-100V, V _{GS} =0V, T _J =150°C | - | - | -100 | |
| Gate-Body Leakage Current | I _{GSS} | V _{GS} = ±20V, V _{DS} =0V | - | - | ±100 | nA |
| Gate Threshold Voltage | V _{GS(th)} | V _{DS} = V _{GS} , I _D =-250μA | -1.2 | -1.7 | -2.2 | V |
| Static Drain-Source On-Resistance | R _{DS(ON)} | V _{GS} =-10V, I _D =-0.8A | - | 440 | 600 | mΩ |
| | | V _{GS} =-4.5V, I _D =-0.8A | - | 480 | 650 | |
| Diode Forward Voltage | V _{SD} | I _S =-0.8A, V _{GS} =0V | - | - | -1.2 | V |
| Gate resistance | R _G | f=1MHz | - | 22 | - | Ω |
| Maximum Body-Diode Continuous Current | I _S | | - | - | -0.8 | A |
| Dynamic Parameters | | | | | | |
| Input Capacitance | C _{iss} | V _{DS} =-50V, V _{GS} =0V, f=1MHz | - | 500 | - | pF |
| Output Capacitance | C _{oss} | | - | 13.3 | - | |
| Reverse Transfer Capacitance | C _{rss} | | - | 10.4 | - | |
| Switching Parameters | | | | | | |
| Total Gate Charge | Q _g | V _{GS} =-10V, V _{DS} =-50V, I _D =-3A | - | 9.5 | - | nC |
| Gate-Source Charge | Q _{gs} | | - | 0.7 | - | |
| Gate-Drain Charge | Q _{gd} | | - | 1.6 | - | |
| Reverse Recovery Charge | Q _{rr} | I _F =-3A, di/dt=100A/us | - | 35 | - | nC |
| Reverse Recovery Time | t _{rr} | | - | 24 | - | ns |
| Turn-on Delay Time | t _{D(on)} | V _{GS} =-10V, V _{DD} =-50V, I _D =-3A R _{GEN} =2.7Ω | - | 4.9 | - | ns |
| Turn-on Rise Time | t _r | | - | 31.3 | - | |
| Turn-off Delay Time | t _{D(off)} | | - | 32.9 | - | |
| Turn-off fall Time | t _f | | - | 34.1 | - | |

Note:

- The entire application environment impacts the thermal resistance values shown, they are not constants and are only valid for the particular conditions noted.
- The value of R_{θJA} is measured with the device mounted on the 40mm*40mm*1.1mm single layer FR-4 PCB board with 1 in² pad of 2oz. Copper, in the still air environment with T_A =25°C. The maximum allowed junction temperature of 150°C. The value in any given application depends on the user's specific board design.



YJL600P10AJ

Typical Electrical and Thermal Characteristics Diagrams

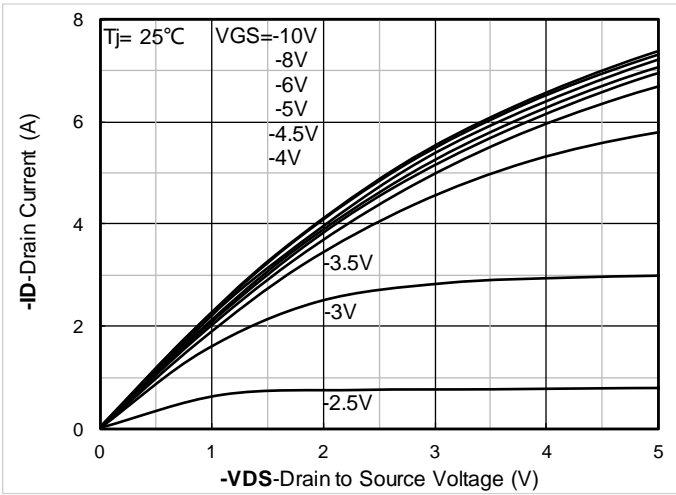


Figure 1. Output Characteristics

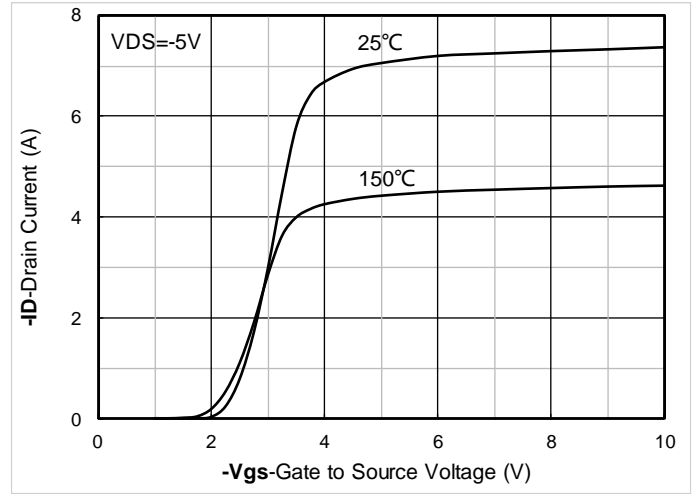


Figure 2. Transfer Characteristics

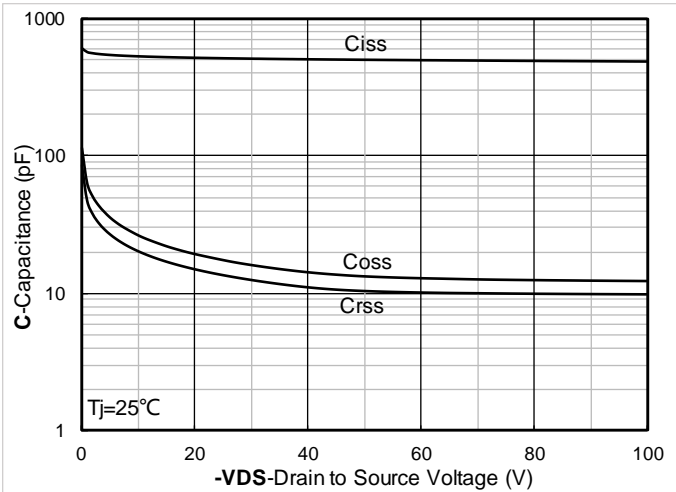


Figure 3. Capacitance Characteristics

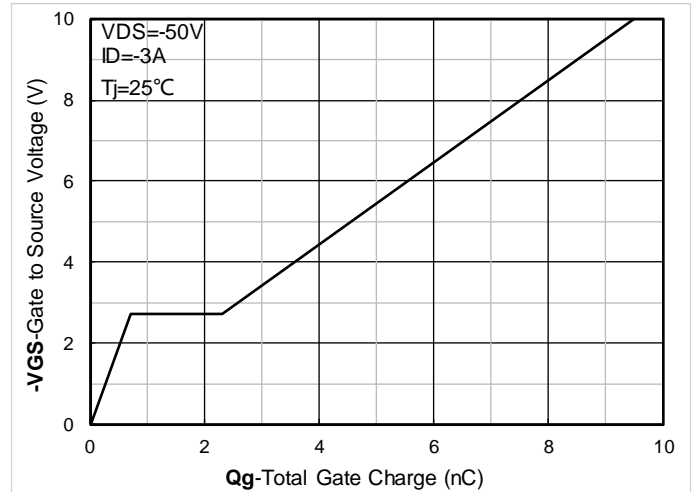


Figure 4. Gate Charge

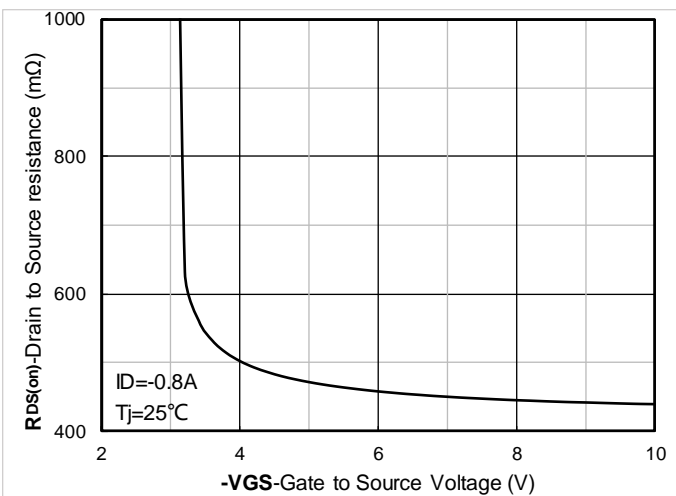


Figure 5. On-Resistance vs Gate to Source Voltage

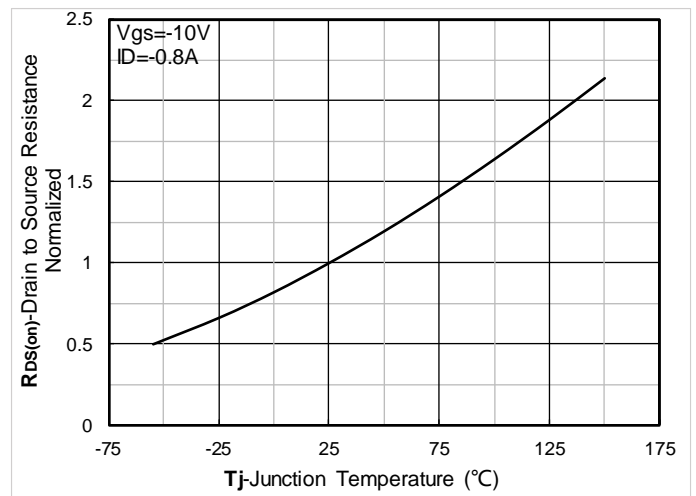


Figure 6. Normalized On-Resistance



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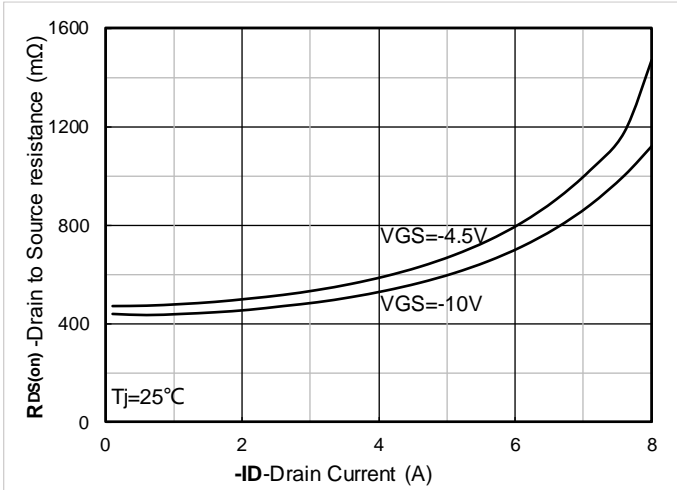


Figure 7. $R_{DS(on)}$ VS Drain Current

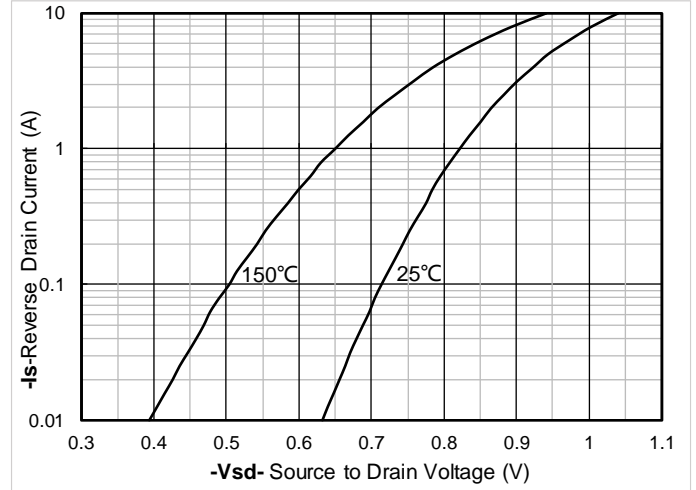


Figure 8. Forward characteristics of reverse diode

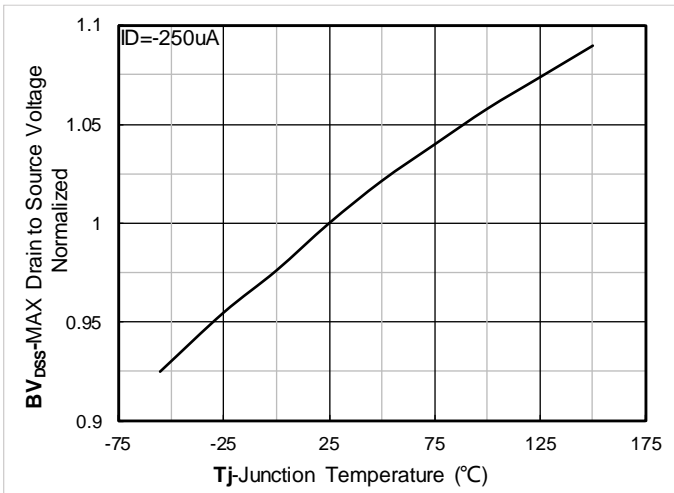


Figure 9. Normalized breakdown voltage

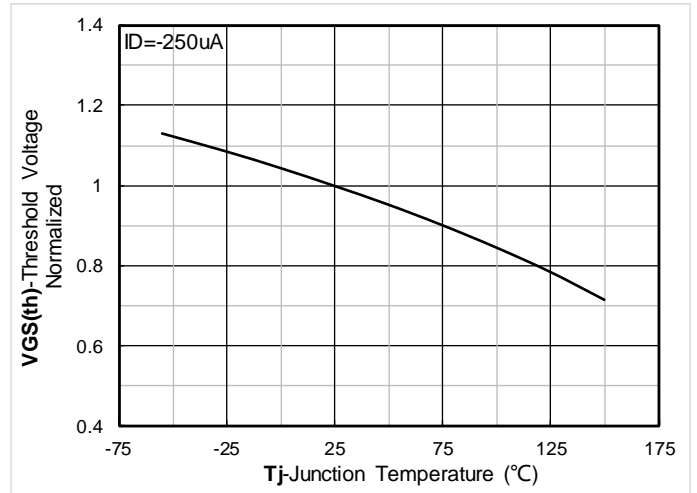


Figure 10. Normalized Threshold voltage

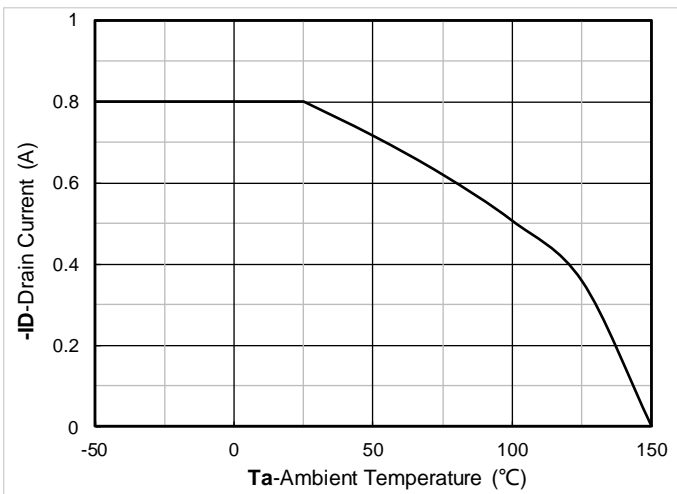


Figure 11. Current dissipation

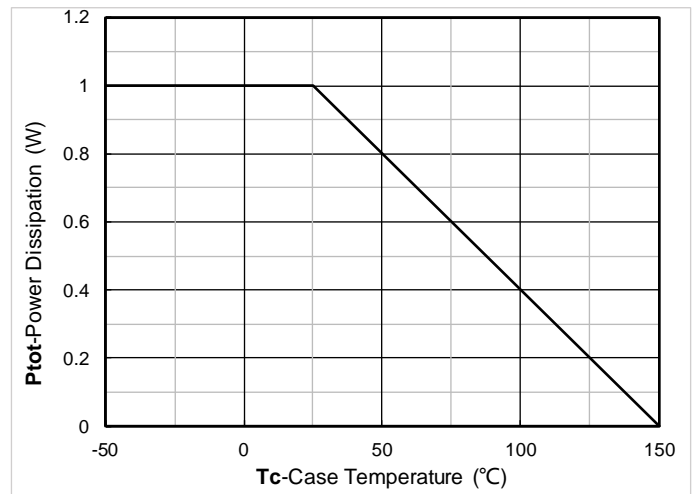


Figure 12. Power dissipation



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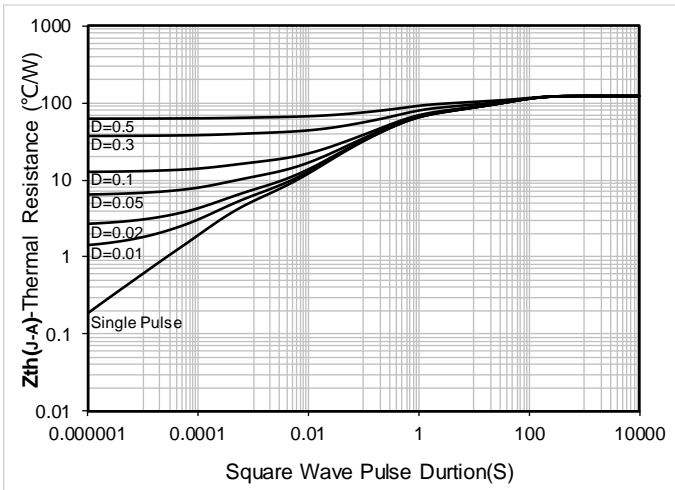


Figure 13. Maximum Transient Thermal Impedance

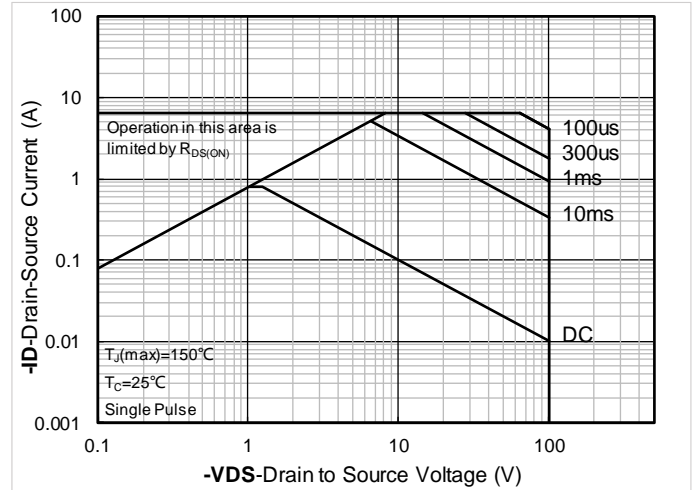
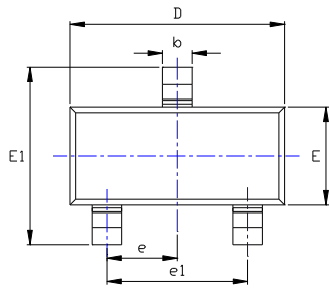


Figure 14. Safe Operation Area

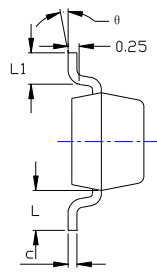


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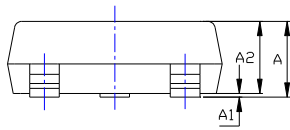
■ SOT-23 Package information



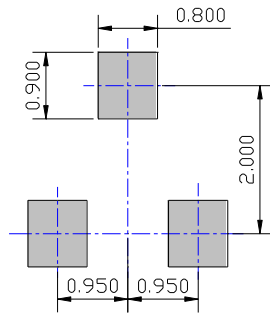
TOP VIEW



SIDE VIEW



SIDE VIEW



UNIT: mm

SUGGESTED SOLDER PAD LAYOUT

| SYMBOL | DIMENSIONS | | | |
|--------|------------|-------|------------|-------|
| | INCHES | | Millimeter | |
| | MIN. | MAX. | MIN. | MAX. |
| A | 0.035 | 0.045 | 0.900 | 1.150 |
| A1 | 0.000 | 0.004 | 0.000 | 0.100 |
| A2 | 0.035 | 0.041 | 0.900 | 1.050 |
| b | 0.012 | 0.020 | 0.300 | 0.500 |
| c | 0.004 | 0.008 | 0.100 | 0.200 |
| D | 0.110 | 0.118 | 2.800 | 3.000 |
| E | 0.047 | 0.055 | 1.200 | 1.400 |
| E1 | 0.089 | 0.100 | 2.250 | 2.550 |
| e | 0.037 TYP | | 0.950 TYP | |
| e1 | 0.071 | 0.079 | 1.800 | 2.000 |
| L | 0.022 REF | | 0.550 REF | |
| L1 | 0.012 | 0.020 | 0.300 | 0.500 |
| θ | 0° | 8° | 0° | 8° |

NOTE:

- 1.PACKAGE BODY SIZES EXCLUDE MOLD FLASH AND GATE BURRS.
- 2.TOLERANCE 0.1mm UNLESS OTHERWISE SPECIFIED.
- 3.THE PAD LAYOUT IS FOR REFERENCE PURPOSES ONLY.



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